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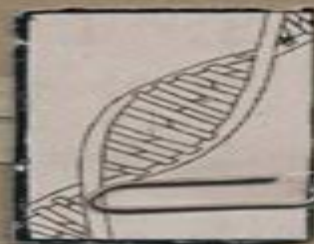
Data Flow Dominated Embedded Software

by

Heiko Falk
Peter Marwedel



```
for (v4y1 = 0; v4y1 < 9; v4y1++) {  
  for (x4 = 0; x4 < 4; x4++) {  
    for (y4 = 0; y4 < 4; y4++) {  
      if (x1 < 0 || 35 < x1 || y1 < 0 || 48 < y1)  
        p1 = 0;  
      else  
        p1 = tmp[(x1*196 + y1) % 464 + (x1*196 + y1) /  
                  464 * 1024 + 496];  
      if (x1 < 0 || 35 < x1 || y1 < 0 || 48 < y1)  
        p2 = 0;  
      else  
        p2 = tmp[(x1*196 + y1) % 496 + (x1*196 + y1) /  
                  496 * 1024];  
    }  
  }  
}
```



Foreword by
Fransky Cathoor

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Source Code Optimization Techniques For Data Flow Dominated Embedded Software

**Institute of Electrical and Electronics
Engineers**



Source Code Optimization Techniques For Data Flow Dominated Embedded Software:

Source Code Optimization Techniques for Data Flow Dominated Embedded Software Heiko Falk, Peter Marwedel, 2013-03-19 This book focuses on source to source code transformations that remove addressing related overhead present in most multimedia or signal processing application programs This approach is complementary to existing compiler technology What is particularly attractive about the transformation flow presented here is that its behavior is nearly independent of the target processor platform and the underlying compiler Hence the different source code transformations developed here lead to impressive performance improvements on most existing processor architecture styles ranging from RISCs like ARM7 or MIPS over Superscalars like Intel Pentium PowerPC DEC Alpha Sun and HP to VLIW DSPs like TI C6x and Philips TriMedia The source code did not have to be modified between processors to obtain these results Apart from the performance improvements the estimated energy is also significantly reduced for a given application run These results were not obtained for academic codes but for realistic and representative applications all selected from the multimedia domain That shows the industrial relevance and importance of this research At the same time the scientific novelty and quality of the contributions have led to several excellent papers that have been published in internationally renowned conferences like e.g. DATE This book is hence of interest for academic researchers both because of the overall description of the methodology and related work context and for the detailed descriptions of the compilation techniques and algorithms

Embedded Software for SoC Ahmed Amine Jerraya, Sungjoo Yoo, Norbert Wehn, Diederik Verkest, 2005-12-30 This title covers all software related aspects of SoC design from embedded and application domain specific operating systems to system architecture for future SoC It will give embedded software designers invaluable insights into the constraints imposed by the use of embedded software in an SoC context

Energy-Efficient Communication Processors Robert Fasthuber, Francky Catthoor, Praveen Raghavan, Frederik Naessens, 2013-05-29 This book describes a new design approach for energy efficient Domain Specific Instruction set Processor DSIP architectures for the wireless baseband domain The innovative techniques presented enable co design of algorithms architectures and technology for efficient implementation of the most advanced technologies To demonstrate the feasibility of the author's design approach case studies are included for crucial functionality of advanced wireless systems with increased computational performance flexibility and reusability Designers using this approach will benefit from reduced development product costs and greater scalability to future process technology nodes

Computer Information Systems and Industrial Management Khalid Saeed, Władysław Homenda, 2018-09-17 This book constitutes the proceedings of the 17th International Conference on Computer Information Systems and Industrial Management Applications CISIM 2018 held in Olomouc Czech Republic in September 2018 The 42 full papers presented together with 4 keynotes were carefully reviewed and selected from 69 submissions The main topics covered by the chapters in this book are biometrics security systems multimedia classification and clustering and industrial management Besides

these the reader will find interesting papers on computer information systems as applied to wireless networks computer graphics and intelligent systems The papers are organized in the following topical sections biometrics and pattern recognition applications computer information systems industrial management and other applications machine learning and high performance computing modelling and optimization and various aspects of computer security Application Analysis Tools for ASIP Design Kingshuk Karuri,Rainer Leupers,2011-06-15 This book introduces a novel design methodology which can significantly reduce the ASIP development effort through high degrees of design automation The key elements of this new design methodology are a powerful application profiler and an automated instruction set customization tool which considerably lighten the burden of mapping a target application to an ASIP architecture in the initial design stages The book includes several design case studies with real life embedded applications to demonstrate how the methodology and the tools can be used in practice for accelerating the overall ASIP design process International Symposium on System Synthesis ,2001 **Advances in Intelligent Modelling and Simulation** Joanna Kołodziej,Samee Ullah Khan,Tadeusz Burczyński,2012-07-11 One of the most challenging issues in today s large scale computational modeling and design is to effectively manage the complex distributed environments such as computational clouds grids ad hoc and P2P networks operating under various types of users with evolving relationships fraught with uncertainties In this context the IT resources and services usually belong to different owners institutions enterprises or individuals and are managed by different administrators Moreover uncertainties are presented to the system at hand in various forms of information that are incomplete imprecise fragmentary or overloading which hinders in the full and precise resolve of the evaluation criteria subsequencing and selection and the assignment scores Intelligent scalable systems enable the flexible routing and charging advanced user interactions and the aggregation and sharing of geographically distributed resources in modern large scale systems This book presents new ideas theories models technologies system architectures and implementation of applications in intelligent scalable computing systems In 15 chapters several important Artificial Intelligence based techniques such as fuzzy logic neural networks evolutionary and memetic algorithms are studied and implemented All of those technologies have formed the foundation for the intelligent scalable computing that we know of today We believe that this book will serve as a reference for students researchers and industry practitioners working or interested in joining interdisciplinary research in the areas of intelligent decision systems using emergent distributed computing paradigms It will also allow newcomers students and researchers alike to grasp key issues and potential solutions on the selected topics This book presents new ideas theories models technologies system architectures and implementation of applications in intelligent scalable computing systems In 15 chapters several important Artificial Intelligence based techniques such as fuzzy logic neural networks evolutionary and memetic algorithms are studied and implemented All of those technologies have formed the foundation for the intelligent scalable computing that we know of today We believe that this book will serve as a reference for students

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Proceedings of the ... International Symposium on Hardware/Software Codesign ,2002 **Evolutionary Hierarchical Multi-Criteria Metaheuristics for Scheduling in Large-Scale Grid Systems** Joanna Kołodziej,2012-07-13 One of the most challenging issues in modelling today s large scale computational systems is to effectively manage highly parametrised distributed environments such as computational grids clouds ad hoc networks and P2P networks Next generation computational grids must provide a wide range of services and high performance computing infrastructures Various types of information and data processed in the large scale dynamic grid environment may be incomplete imprecise and fragmented which complicates the specification of proper evaluation criteria and which affects both the availability of resources and the final collective decisions of users The complexity of grid architectures and grid management may also contribute towards higher energy consumption All of these issues necessitate the development of intelligent resource management techniques which are capable of capturing all of this complexity and optimising meaningful metrics for a wide range of grid applications This book covers hot topics in the design administration and management of dynamic grid environments with a special emphasis on the preferences and autonomous decisions of system users secure access to the processed data and services and application of green technologies It features advanced research related to scalable genetic based heuristic approaches to grid scheduling whereby new scheduling criteria such as system reliability security and energy consumption are incorporated into a general scheduling model This book may be a valuable reference for students researchers and practitioners who work on or who are interested in joining interdisciplinary research efforts in the areas of distributed and evolutionary computation

Deutsche Nationalbibliographie und Bibliographie der im Ausland erschienenen deutschsprachigen Veröffentlichungen ,2004 **Proceedings of Technical Papers** ,2005 Integrated Circuit and System Design ,2005 **IEEE International High-Level Design Validation and Test Workshop** ,2003 The British National Bibliography Arthur James Wells,2005 Proceedings ,1997 **Memorandum** ,2000 **ACM SIGPLAN Notices** ,2006-07 Proceedings of the 12th International Symposium on System Synthesis ,1999 These proceedings cover real time and low power system design performance issues in system design memory design for embedded systems architectural synthesis and system design methodologies

Electronic Design , *Proceedings* Institute of Electrical and Electronics Engineers,1993 The Workshop held in Oulu Finland June 1993 is a forum that covers the state of the art research and development in real time computing These selected papers cover a wide range of issues such as modeling architecture scheduling operating systems prototyping timing analysis fault toleranc

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