

# [ Basic Features of VHDL ]

## ■ Sequential Control Statements

### • *If* Statement

```
if {condition1} then
    {sequence_of_statements_1}
elsif {condition2} then
    {sequence_of_statements_2}
    ...
    ...
else {sequence_of_statements_n}
end if;
```

```
if A < 0 then
    LEVEL := 1;
elsif A > 10 then
    LEVEL := 3;
else LEVEL := 2;
end if;
```

# Structured Logic Design With Vhdl

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*Structured Logic Design with VHDL* James R. Armstrong, F. Gail Gray, 1993 Hardware Logic Design      *Circuit Design: Know It All* Darren Ashby, Bonnie Baker, Ian Hickman, Walt Kester, Robert Pease, Tim Williams, Bob Zeidman, 2011-04-19 The Newnes Know It All Series takes the best of what our authors have written to create hard working desk references that will be an engineer's first port of call for key information design techniques and rules of thumb Guaranteed not to gather dust on a shelf Electronics Engineers need to master a wide area of topics to excel The Circuit Design Know It All covers every angle including semiconductors IC Design and Fabrication Computer Aided Design as well as Programmable Logic Design A 360 degree view from our best selling authors Topics include fundamentals Analog Linear and Digital circuits The ultimate hard working desk reference all the essential information techniques and tricks of the trade in one volume      **Proceedings of the IEEE 1992 National Aerospace and Electronics Conference, NAECON 1992**, 1992      **Official Gazette of the United States Patent and Trademark Office** United States. Patent and Trademark Office, 1996-08      **Introduction to Digital Systems** Milos D. Ercegovac, Tom's Lang, Jaime H. Moreno, 1998-10-29 This manual covers more topics related to the field advanced implementation of algorithmic systems and advanced material on VHDL design A real emphasis is placed on the hierarchical approach to the design of digital systems and is followed consistently throughout the text      Smart Structures and Materials, 1999      **Successful ASIC Design the First Time Through** John Huber, Mark W. Rosneck, 1991-06-27 The very name application specific integrated circuit or ASIC connotes an ability to provide a dense package for a highly complex design targeted at a focused often complex solution The ability to create customized high performance designs has come of age facilitated by sophisticated tools that enable designers to cope with ever increasing demands for added product functionality features and complexity Most designers are trained in the traditional methods of approaching complex digital electronics with standard parts but have little if any exposure to custom or even semicustom integrated circuit design Most see only a broad survey of IC technology This book is targeted at the new ASIC designer who is getting ready to tackle that first ASIC design and is concerned about the unknowns that lie ahead Economic and performance considerations as well as tool capability and process fabrication quality have evolved to the point where consideration of ASIC design is now commonplace in an ever increasing number of electronic systems designs Engineers are now given the challenge of coping not only with new technologies but with new design methodologies that are fundamentally necessary and advantageous to support new competitive high tech products Laypeople and engineers alike have marveled at the advances made over the years in electronics complexity performance density and cost The migration of systems to modules to boards to integrated circuits clearly underscores the radical transition that the physical incarnation of electronics has undergone      Electronic Design, 1994      Verilog Digital System Design Zainalabedin Navabi, 1999 Annotation A much needed step by step tutorial to designing with Verilog one of the most popular hardware description languages Each chapter

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Embedded Systems Programming, 1997

*Proceedings* American Society for Engineering Education. Conference, 1995

IEEE VLSI Test Symposium, 1999

**Computer Aided Logical Design with Emphasis on VLSI** Frederick J. Hill, Gerald R. Peterson, 1993-02-11 Tied to no particular set of computer aided logic design tools it advocates the new emphasis in VLSI design Includes support of layout synthesis from description in a register transfer level language as well as from design capture Contains a detailed introduction to Boolean algebra Karnaugh maps and sequential circuits In this edition discussion of combination logic has been extended switching circuits updated a comprehensive treatment of test generation for VLSI included

**Annual Conference Proceedings** American Society for Engineering Education. Conference, 1995

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2000 22nd International Conference on Microelectronics IEEE Electron Devices Society, International Conference on Microelectronics, 1999 The papers in this volume are a partial selection from the International Conference on Microelectronic 1999 which provides a forum for the presentation and discussion of the recent developments and future trends in the field of microelectronics

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