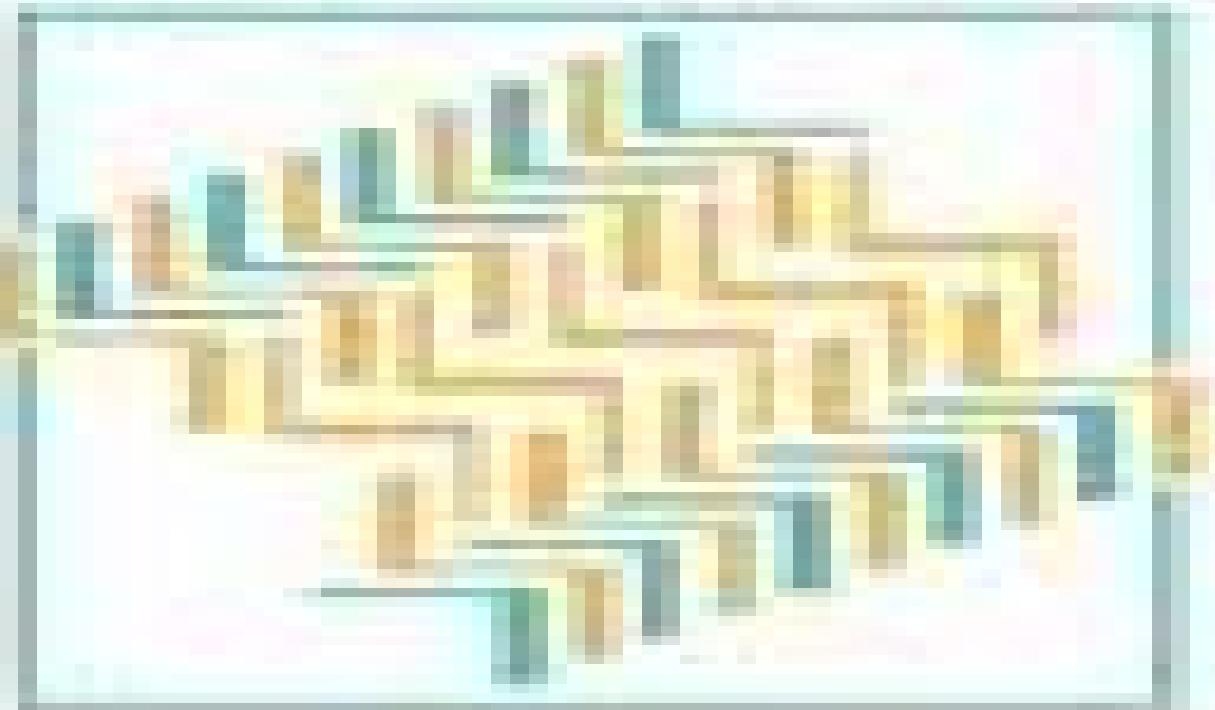


# SWITCHING THEORY FOR LOGIC SYNTHESIS



By: **John Doe**

Version: 1.0

# Switching Theory For Logic Synthesis

J Ma

## **Switching Theory For Logic Synthesis:**

Switching Theory for Logic Synthesis Tsutomu Sasao,2012-12-06 Switching Theory for Logic Synthesis covers the basic topics of switching theory and logic synthesis in fourteen chapters Chapters 1 through 5 provide the mathematical foundation Chapters 6 through 8 include an introduction to sequential circuits optimization of sequential machines and asynchronous sequential circuits Chapters 9 through 14 are the main feature of the book These chapters introduce and explain various topics that make up the subject of logic synthesis multi valued input two valued output function logic design for PLDs FPGAs EXOR based design and complexity theories of logic networks An appendix providing a history of switching theory is included The reference list consists of over four hundred entries Switching Theory for Logic Synthesis is based on the author's lectures at Kyushu Institute of Technology as well as seminars for CAD engineers from various Japanese technology companies

Switching Theory for Logic Synthesis will be of interest to CAD professionals and students at the advanced level It is also useful as a textbook as each chapter contains examples illustrations and exercises

*Fundamentals of Logic Design and Switching Theory* Arthur D. Friedman,1986

*Progress in Applications of Boolean Functions* Tsutomu Sasao,Jon

Butler,2022-05-31 This book brings together five topics on the application of Boolean functions They are 1 Equivalence classes of Boolean functions The number of  $n$  variable functions is large even for values as small as  $n=6$  and there has been much research on classifying functions There are many classifications each with their own distinct merit 2 Boolean functions for cryptography The process of encrypting decrypting plaintext messages often depends on Boolean functions with specific properties For example highly nonlinear functions are valued because they are less susceptible to linear attacks 3 Boolean differential calculus An operation analogous to taking the derivative of a real valued function offers important insight into the properties of Boolean functions One can determine tests or susceptibility to hazards 4 Reversible logic Most logic functions are irreversible it is impossible to reconstruct the input given the output However Boolean functions that are reversible are necessary for quantum computing and hold significant promise for low power computing 5 Data mining The process of extracting subtle patterns from enormous amounts of data has benefited from the use of a graph based representation of Boolean functions This has use in surveillance fraud detection scientific discovery including bio informatics genetics medicine and education Written by experts these chapters present a tutorial view of new and emerging technologies in Boolean functions Table of Contents Equivalence Classes of Boolean Functions Boolean Functions for Cryptography Boolean Differential Calculus Synthesis of Boolean Functions in Reversible Logic Data Mining Using Binary Decision Diagrams

Applications of Zero-Suppressed Decision Diagrams Tsutomu Sasao,Jon T. Butler,2014-11-01 A zero suppressed decision diagram ZDD is a data structure to represent objects that typically contain many zeros Applications include combinatorial problems such as graphs circuits faults and data mining This book consists of four chapters on the applications of ZDDs The first chapter by Alan Mishchenko introduces the ZDD It compares ZDDs to BDDs showing why a more compact

representation is usually achieved in a ZDD. The focus is on sets of subsets and on sum of products SOP expressions. Methods to generate all the prime implicants PIs and to generate irredundant SOPs are shown. A list of papers on the applications of ZDDs is also presented. In the appendix ZDD procedures in the CUDD package are described. The second chapter by Tsutomu Sasao shows methods to generate PIs and irredundant SOPs using a divide and conquer method. This chapter helps the reader to understand the methods presented in the first chapter. The third chapter by Shin Ichi Minato introduces the frontier based method that efficiently enumerates certain subsets of a graph. The final chapter by Shinobu Nagayama shows a method to match strings of characters. This is important in routers for example where one must match the address information of an internet packet to the proper output port. It shows that ZDDs are more compact than BDDs in solving this important problem. Each chapter contains exercises and the appendix contains their solutions. Table of Contents, Preface, Acknowledgments, Introduction to Zero Suppressed Decision Diagrams, Efficient Generation of Prime Implicants and Irredundant Sum of Products Expressions, The Power of Enumeration, BDD, ZDD Based Algorithms for Tackling Combinatorial Explosion, Regular Expression Matching Using Zero Suppressed Decision Diagrams, Authors and Editors Biographies, Index.

**Switching Theory & Logic Design** Atul P. Godse, 2009. Number Systems and Codes, Philosophy of number systems, complement representation of negative numbers, binary arithmetic, binary codes, error detecting and error correcting codes, hamming codes, Boolean Algebra and Switching Functions, Fundamental postulates of Boolean Algebra, Basic theorems and properties, switching functions, Canonical and Standard forms, Algebraic simplification, digital logic gates, properties of XOR gates, universal gates, Multilevel NAND NOR realizations, Minimization of Switching Functions, Map method, Prime implicants, Don't care combinations, Minimal SOP and POS forms, Tabular Method, Prime Implicant chart, simplification rules, Combinational Logic Design, Design using conventional logic gates, Encoder, Decoder, Multiplexer, De Multiplexer, Modular design, IC chips, MUX, Realization of switching functions, Parity bit generator, Code converters, Hazards and hazard free realizations, Programmable Logic Devices, Threshold Logic, Basic PLD's, ROM, PROM, PLA, PLD, Realization of Switching functions using PLD's, Capabilities and limitations of Threshold gate, Synthesis of Threshold functions, Multigate, Synthesis Sequential Circuits, IClassification of sequential circuits, Synchronous, Asynchronous, Pulse mode, Level mode with examples, Basic flop flops, Triggering and excitation tables, Steps in synchronous sequential circuit design, Design of modulo N Ring and shift counters, Serial binary adder, sequence detector, Sequential Circuits, IIFinite state machine capabilities and limitations, Mealy and Moore models, minimization of completely specified and incompletely specified sequential machines, Partition techniques and Merger chart methods, concept of minimal cover table, Algorithmic State Machines, Salient features of the ASM chart, Simple examples, System design using data path and control subsystems, control implementations, examples of Weighing machine and Binary multiplier.

**Logic Design and Switching Theory** Saburo Muroga, 1979-05. Good. No Highlights, No Markup. all pages are intact. Slight Shelfwear may have the corners slightly dented, may have slight color changes, slightly damaged spine.

**Advances in Computers** ,1991-07-15 Advances in Computers      *NBS Special Publication* ,1968      **Fundamentals of Switching Theory and Logic Design** Jaakko Astola, Radomir S. Stankovic, 2006-03-07 Fundamentals of Switching Theory and Logic Design discusses the basics of switching theory and logic design from a slightly alternative point of view and also presents links between switching theory and related areas of signal processing and system theory. Switching theory is a branch of applied mathematics providing mathematical foundations for logic design which can be considered as a part of digital system design concerning realizations of systems whose inputs and outputs are described by logic functions.

*Brinkman's cumulatieve catalogus van boeken, en verder in den boekhandel voorkomende artikelen* ,2000 Voorts een alfabetische lijst van Nederlandsche boeken in België uitgegeven      **Computer Literature Bibliography: 1964-1967** W.

W. Youden, 1965      **The Dhaka University Journal of Science** ,1998      *Cybernetics Abstracts* ,1966      **Synthesis of**

**Switching Circuits** George J. Klir, Lev K. Seidl, 1968      **15th Symposium on Integrated Circuits and Systems Design**

Ricardo Augusto da Luz Reis, Ney Calazans, 2002      **Boolean Problems** Bernd Steinbach, 2002      **Novi Sad Journal of**

**Mathematics** ,2000      **SBCCI 2006** ,2006      *Introduction to Logic Design* Svetlana N. Yanushkevich, Vlad P.

Shmerko, 2008-01-25 With an abundance of insightful examples, problems, and computer experiments, *Introduction to Logic Design* provides a balanced, easy-to-read treatment of the fundamental theory of logic functions and applications to the design of digital devices and systems. Requiring no prior knowledge of electrical circuits or electronics, it supplies the essential material to understand the basic operation and design of digital systems. Satisfying the requirements of an introductory course, the text presents a clear picture of basic concepts, effective problem-solving techniques, modern technologies, and applications. It focuses on the relationships between and the manipulation of various data structures. By examining the similarities and differences between assorted theoretical approaches for the representation, manipulation, and optimization of Boolean functions, the authors provide a unified overview of the relationships among digital system design, computer organization, micro and nanoelectronics, and numerical methods. The book also describes classical minimization techniques, along with advanced and emerging problems of logic design. Meeting the needs of developing technology, reflecting the integrated nature of modern engineering, this text shows how theoretical ideas, physical devices, and design methodologies come together to form a successful design approach. It provides the basis to explore even more sophisticated aspects in the field.      **SBCCI 2007** Antonio Petraglia, Association for Computing Machinery, 2007

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