SYNTHESIS & OPTIMIZATION OF DIGITAL CIRCUITS

Synthesis And Optimization Of Digital Circuits

Qian-Ling Zhang

Synthesis And Optimization Of Digital Circuits:

  Synthesis and Optimization of Digital Circuits Giovanni De Micheli,1994 The book provides a thorough explanation of synthesis and optimization algorithms accompanied by a sound mathematical formulation and a unified notation **Logic Synthesis and Optimization** Tsutomu Sasao,2012-12-06 Logic Synthesis and Optimization presents up to date research information in a pedagogical form The authors are recognized as the leading experts on the subject The focus of the book is on logic minimization and includes such topics as two level minimization multi level minimization application of binary decision diagrams delay optimization asynchronous circuits spectral method for logic design field programmable gate array FPGA design EXOR logic synthesis and technology mapping Examples and illustrations are included so that each contribution can be read independently Logic Synthesis and Optimization is an indispensable reference for academic researchers as well as professional CAD engineers **Synthesis and Optimization of FPGA-Based Systems** Valery Sklyarov,Iouliia Skliarova,Alexander Barkalov,Larysa Titarenko,2014-03-14 The book is composed of two parts The first part introduces the concepts of the design of digital systems using contemporary field programmable gate arrays FPGAs Various design techniques are discussed and illustrated by examples The operation and effectiveness of these techniques is demonstrated through experiments that use relatively cheap prototyping boards that are widely available The book begins with easily understandable introductory sections continues with commonly used digital circuits and then gradually extends to more advanced topics The advanced topics include novel techniques where parallelism is applied extensively These techniques involve not only core reconfigurable logical elements but also use embedded blocks such as memories and digital signal processing slices and interactions with general purpose and application specific computing systems Fully synthesizable specifications are provided in a hardware description language VHDL and are ready to be tested and incorporated in engineering designs A number of practical applications are discussed from areas such as data processing and vector based computations e g Hamming weight counters comparators The second part of the book covers the more theoretical aspects of finite state machine synthesis with the main objective of reducing basic FPGA resources minimizing delays and achieving greater optimization of circuits and systems **IEEE Circuits & Devices** ,1996 **Representing and Modeling Digital Circuits** Tomas Gerhard Rokicki,1993 **Readings in Hardware/Software Co-Design** Giovanni De Micheli,Rolf Ernst,Wayne Wolf,2002 This title serves as an introduction ans reference for the field with the papers that have shaped the hardware software co design since its inception in the early 90s *Journal of VLSI Signal Processing Systems for Signal, Image, and Video Technology* ,1997 **Logic Synthesis for FSM-Based Control Units** Alexander Barkalov,Larysa Titarenko,2009-11-25 This book presents the hardware implementation of control algorithms represented by graph schemes of algorithm It includes new methods of logic synthesis and optimization for logic circuits of Mealy and Moore FSMs oriented on both ASIC and FPLD **Proceedings of the ... Midwest Symposium on Circuits and Systems** ,1999 <u>1996 2nd</u>

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Luk,2007-05-08 Synthesis and Optimization of DSP Algorithms describes approaches taken to synthesising structural hardware descriptions of digital circuits from high level descriptions of Digital Signal Processing DSP algorithms The book contains A tutorial on the subjects of digital design and architectural synthesis intended for DSP engineers A tutorial on the subject of DSP intended for digital designers A discussion of techniques for estimating the peak values likely to occur in a DSP system thus enabling an appropriate signal scaling Analytic techniques simulation techniques and hybrids are discussed The applicability of different analytic approaches to different types of DSP design is covered The development of techniques to optimise the precision requirements of a DSP algorithm aiming for efficient implementation in a custom parallel processor The idea is to trade off numerical accuracy for area or power consumption advantages Again both analytic and simulation techniques for estimating numerical accuracy are described and contrasted Optimum and heuristic approaches to precision optimisation are discussed A discussion of the importance of the scheduling allocation and binding problems and development of techniques to automate these processes with reference to a precision optimized algorithm Future perspectives for synthesis and optimization of DSP algorithms *Proceedings of Euro ASIC '90* Institut national polytechnique (Grenoble). Club conception de circuits à la demande,1990 *VHDL: Basics to Programming* Gaganpreet Kaur,2011 VHDL Basics to Programming is ideal for undergraduate and graduate students of Electronics Engineering Computer Engineering and Information Technology VHDL Basics to Programming is a methodological guide to VHDL and its applications It Euro ASIC ,1990

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