

Special Computer Architectures for Pattern Processing

Edited by
King-Sun Fu, Tadao Ichikawa

Special Computer Architectures For Pattern Processing

Jiyuan Zhang

Special Computer Architectures For Pattern Processing:

Special Computer Architectures for Pattern Processing King-Sun Fu,2018-05-04 It has been recognized for a long time that a conventional sequential processor is inefficient for operations on pictorial data where relatively simple operations need to be performed on a large number of data elements pixels Though many parallel processing architectures for picture processing have been proposed in the past very few have actually been implemented due to the costs involved With LSI technology it is becoming possible to realize parallel architectures at a modest cost In the following the authors review some of the proposed architectures for pattern recognition and image processing

1985 IEEE Computer Society Workshop

on Computer Architecture for Pattern Analysis and Image Database Management, Miami Beach, Florida,

November 18-20, 1985 IEEE Computer Society,1985 **Multicomputer Vision** S. Levialdi,1988 **Computer**

Architecture, Tutorial Daniel D. Gajski,1987 This tutorial is intended for computer system architects designers and managers who need a broad range of knowledge on advanced topics in computer architecture The book can be used as a textbook or as a research and design reference The goal of this tutorial is to present the state of the art in advanced computer architecture Part I deals with the concepts underlying current architectures Part II covers a variety of approaches and techniques being used in the design of advanced computer systems

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Handbook Norman G. Einspruch,1985 **ACM SIGPLAN Notices** ,2006-07 **Computers in Building** Godfried Augenbroe,Charles Eastman,1999-05-31 Proceedings of the CAAD Futures 99 Conference

Directory of Published Proceedings ,2002 *XI Brazilian Symposium on Integrated Circuit Design* Marcelo Lubaszewski,Vladimir Castro

Alves,1998 Topics in this book on integrated circuit design include hardware software codesign of embedded systems the ALFA HUERTA project rapid prototyping digital testing and digital design **IEEE Proceedings of the Southeastcon** ,1987 *Architectural Program Report, July 1992* University of California, Berkeley. Department of Architecture,1992

Guide to Architecture Schools in North America ,1989 *Process and Pattern in Evolution* Terrell H.

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