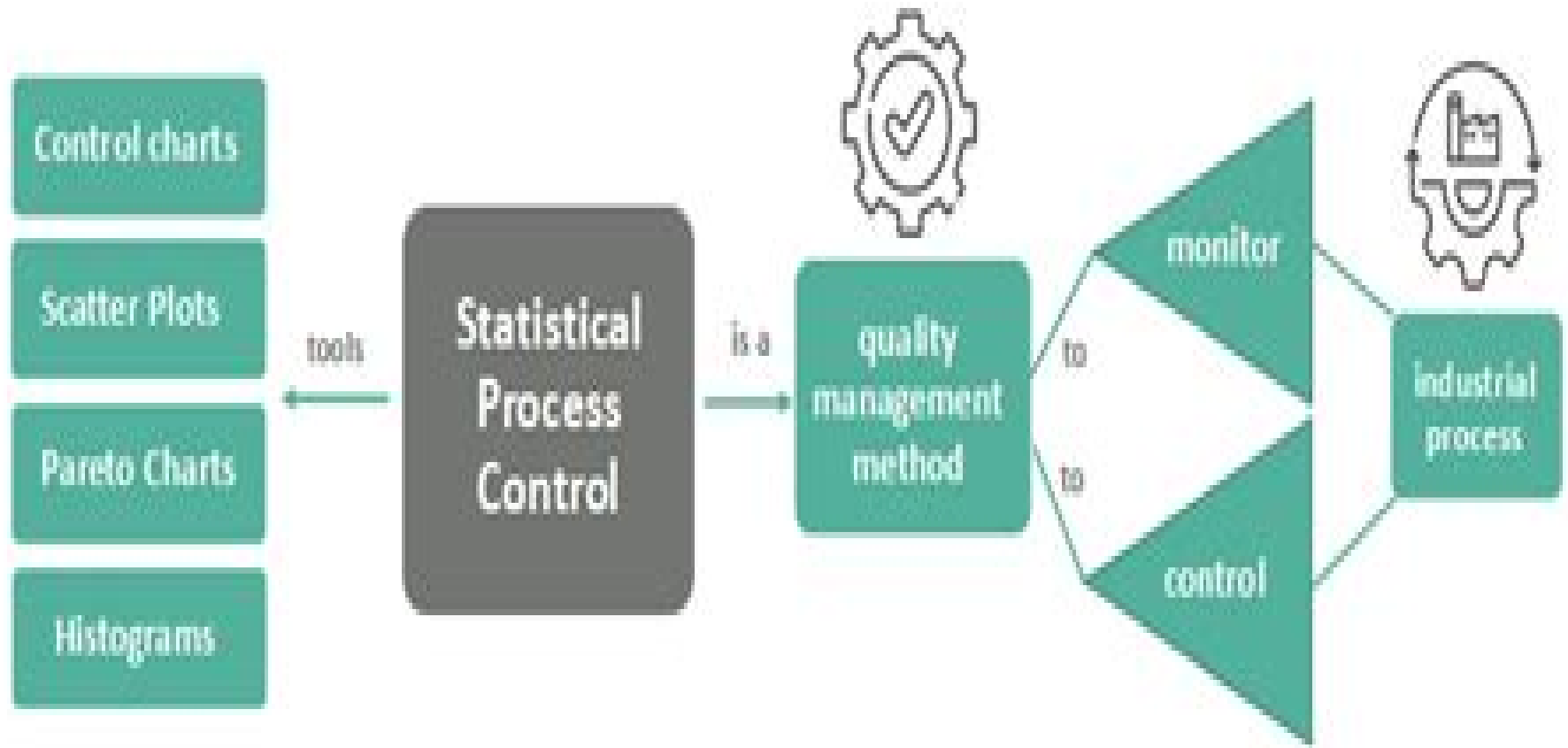


Statistical Process Control



Statistical Process Control For Surface Mount Technology

James K. Hollomon



Statistical Process Control For Surface Mount Technology:

Statistical Process Control for Surface Mount Technology William Samuel Messina, 1999 The book is divided into two parts Part I provides an overview of the methods of data slejthing Part II provides a case study approach to the implementation of SPC throughout the whole SMT manufacturing line It starts with how to qualify the raw material from the supplier to the implementation of SPC for final test of the printed circuit board **Surface Mount Technology** Ray P. Prasad, 2012-12-06 Surface Mount Technology is not a technology of tommorrow but a technology of today It provides a quantum jump in the packaging tech nology to produce state of the art miniaturized electronic products How ever in order to take advantage of this technology a complete infrastruc ture must be put in place This requires considerable investment in human and capital resources Intel corporation has made these investments to keep its customers for components and systems on the leading edge of technology Based on the experience of putting this infrastructure in place for system products this book is written for managers who need to manage the risk during its implementation and the practicing engineers who need to improve the design and manufacturing processes for improved yield and cost reduction To accomplish this task I have not only culled the infor mation from published materials but have also depended on input from both my colleagues in Intel and such outside organizations as the Institute of interconnecting and Packaging electronic Circuits IPC the Electronics Industries Association EIA and the Surface Mount Council But the underlying basis for this book has been my first hand experience in im plementing this technology for Intel Systems Group and my experience at Boeing my previous employer In a fast changing technology like SMT it is very easy to have obsolete information even before the book is published For this reason I have concentrated on the basic principles and practice of the technology Statistical Case Studies for Industrial Process Improvement Veronica Czitrom, Patrick D. Spagon, 1997-01-01 This book contains a broad selection of case studies written by professionals in the semiconductor industry that illustrate the use of statistical methods to improve manufacturing processes These case studies offer engineers scientists technicians and managers numerous examples of best in class practices by their peers Because of the universal nature of statistical applications the methods described here can be applied to a wide range of industries including the chemical biotechnology automotive steel plastics textile and food industries Many industries already benefit from the use of statistical methods although the semiconductor industry is considered both a leader in and a model for the wide application and effective use of statistics Surface Mount Technology with Fine Pitch Components H. Danielsson, 1995 This is a state of the art guide to SMT with fine pitch components intended for professionals in electronics manufacturing The overriding objective is to equip manufacturing people in the electronics industry with a better understanding of the manufacturing processes involved Handbook of Fine Pitch Surface Mount Technology John H. Lau, 1994 Surface mount technology SMT is a mature technology SMT allows placement of more surface mount components SMC into smaller and tighter printed circuit board PCB areas This increased density means increased

performance and power in smaller packaging systems and allows manufacturing of smaller and higher performance products at lower cost. The advance of integrated circuit IC technology and the requirements of high density for high speed circuitry is driving the design of SMC to higher pin count and smaller package size. In general the higher pin count and smaller package size are accomplished by reducing the bond pad size and spacing pitch on the chip level and the lead pin solder dimensions and pitch on the chip carrier module level. The last few years have witnessed an explosive growth in the research and development efforts devoted to FPT as a direct result of the rapid growth of SMT and miniaturization. Some examples are hand held lightweight video recorders that can take sharp pictures, hand held lightweight devices that can track the worldwide package movements and portable computers with tiny yet powerful microprocessors and large memory capability that can fit into a briefcase or into the palm of your hand. *Surface-mount Technology for PC Boards* Glenn R.

Blackwell, James K. Hollomon, 2006. Learn to generate high manufacturing yields, low testing costs and reproducible designs using the latest components of surface mount technology. SMT Manufacturers, managers, engineers, students and others who work with printed circuit boards will find a wealth of cutting edge information about SMT and fine pitch technology. FPT in this new edition. Practical data and clear illustrations combine to clearly and accurately present the details of design for manufacturability, environmental compliance, design for test and quality reliability for today's miniaturized electronics packaging.

Electronic Materials Handbook, 1989-11-01. Volume 1. Packaging is an authoritative reference source of practical information for the design or process engineer who must make informed day to day decisions about the materials and processes of microelectronic packaging. Its 117 articles offer the collective knowledge, wisdom and judgement of 407 microelectronics packaging experts, authors, co-authors and reviewers representing 192 companies, universities, laboratories and other organizations. This is the inaugural volume of ASM's all new Electronic Materials Handbook series designed to be the Metals Handbook of electronics technology. In over 65 years of publishing the Metals Handbook, ASM has developed a unique editorial method of compiling large technical reference books. ASM's access to leading materials technology experts enables to organize these books on an industry consensus basis. Behind every article is an author who is a top expert in its specific subject area. This multi-author approach ensures the best, most timely information throughout. Individually selected panels of 5 and 6 peers review each article for technical accuracy, generic point of view and completeness. Volumes in the Electronic Materials Handbook series are multidisciplinary to reflect industry practice applied in integrating multiple technology disciplines necessary to any program in advanced electronics. Volume 1, Packaging, focusing on the middle level of the electronics technology size spectrum, offers the greatest practical value to the largest and broadest group of users. Future volumes in the series will address topics on larger integrated electronic assemblies and smaller semiconductor materials and devices size levels. *Surface-mount Technology for PC Board Design* James K. Hollomon, 1989.

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