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Systematic Modeling And Analysis Of Telecom Frontends And Their Building Blocks

**Piet Vanassche, Georges Gielen, Willy
M Sansen**



Systematic Modeling And Analysis Of Telecom Frontends And Their Building Blocks:

Systematic Modeling and Analysis of Telecom Frontends and their Building Blocks Piet Vanassche, Georges Gielen, Willy M. C. Sansen, 2005-09-01 To meet the demands of today's highly competitive market analog electronics designers must develop their IC designs in a minimum of time. The difference between first and second time right seriously affects a company's share of the market. Analog designers are therefore in need for structured design methods together with the theory and tools to support them especially when pushing the performance limits in high performance designs. Systematic Modeling and Analysis of Telecom Frontends and Their Building Blocks aims to help designers in speeding up telecommunication frontend design by offering an in depth understanding of the frontend's behavior together with methods and algorithms that support designers in bringing this understanding to practice. The book treats topics such as time varying phase locked loop stability, noise in mixing circuits, oscillator injection locking, oscillator phase noise behavior, harmonic oscillator dynamics and many more. In doing so it always starts from a theoretical foundation that is both rigorous and general. Phase locked loop and mixer analysis for example are grounded upon a general framework for time varying small signal analysis. Likewise analysis of harmonic oscillator transient behavior and oscillator phase noise analysis are treated as particular applications of a general framework for oscillator perturbation analysis. In order to make the book as easy to read as possible all theory is always accompanied by numerous examples and easy to catch intuitive explanations. As such the book is suited for both computer aided design engineers looking for general theories and methods either as background material or for practical implementation in tools as well as for practicing circuit designers looking for help and insight in dealing with a particular application or a particular high performance design problem.

Systematic Modeling and Analysis of Telecom Frontends and their Building Blocks Piet Vanassche, Georges Gielen, Willy M. Sansen, 2005-10-24 To meet the demands of today's highly competitive market analog electronics designers must develop their IC designs in a minimum of time. The difference between first and second time right seriously affects a company's share of the market. Analog designers are therefore in need for structured design methods together with the theory and tools to support them especially when pushing the performance limits in high performance designs. Systematic Modeling and Analysis of Telecom Frontends and Their Building Blocks aims to help designers in speeding up telecommunication frontend design by offering an in depth understanding of the frontend's behavior together with methods and algorithms that support designers in bringing this understanding to practice. The book treats topics such as time varying phase locked loop stability, noise in mixing circuits, oscillator injection locking, oscillator phase noise behavior, harmonic oscillator dynamics and many more. In doing so it always starts from a theoretical foundation that is both rigorous and general. Phase locked loop and mixer analysis for example are grounded upon a general framework for time varying small signal analysis. Likewise analysis of harmonic oscillator transient behavior and oscillator phase noise analysis are treated as particular applications of a general framework for oscillator

perturbation analysis In order to make the book as easy to read as possible all theory is always accompanied by numerous examples and easy to catch intuitive explanations As such the book is suited for both computer aided design engineers looking for general theories and methods either as background material or for practical implementation in tools as well as for practicing circuit designers looking for help and insight in dealing with a particular application or a particular high performance design problem

High-Level Modeling and Synthesis of Analog Integrated Systems Ewout S. J.

Martens, Georges Gielen, 2008-01-03 As the miniaturization of semiconductor technology continues electronic systems on chips offer a more extensive and more complex functionality with better performance higher frequencies and less power consumption Whereas digital designers can take full advantage of the availability of design automation tools to build huge systems the lack of support by computer programs for different abstraction levels makes analog design a time consuming handcraft which limits the possibilities to implement large systems Various approaches for finding optimal values for the parameters of analog cells like opamps have been investigated since the mid 1980s and they have made their entrance in commercial applications However a larger impact on the performance is expected if tools are developed which operate on a higher abstraction level and consider multiple architectural choices to realize a particular functionality In this book the opportunities conditions problems solutions and systematic methodologies for this new generation of analog CAD tools are examined

The outline of this book is as follows In the first part the characteristics of the analog design process are systematically analyzed and several approaches for automated analog synthesis are summarized Comparison of their properties with the requirements for high level synthesis of analog and mixed signal systems results in a new design paradigm the high level design flow based on generic behavior This design approach involves a modeling strategy using generic behavioral models and a synthesis strategy leading to the exploration of a heterogeneous design space containing different architectures The modeling strategy is further elaborated in Part II *IEEE Transactions on Circuits and Systems*, 2006 *Electrical & Electronics Abstracts*, 1997

Documentation Abstracts, 1998 *The International Journal of Microcircuits and Electronic Packaging*, 2000 1998
International Conference on Parallel and Distributed Systems IEEE Computer Society. TC on Distributed Processing, 1998
Proceedings of the December 1998 conference One hundred contributions cover architecture mobile computing Internet technology database systems and applications multimedia interconnection network high speed networking parallel distributed computing and system supports fault tolerance real time and compilation for parallelism Contains an author list but no subject index Annotation copyrighted by Book News Inc Portland OR *Proceedings*, 2004 **Conference Proceedings**, 2003 **Journal of Telecommunication Networks**, 1984 *CIEP ...*, 2004 **Alcatel Telecommunications Review**, 1996 **Telecommunications**, 1983 *MASCOTS '95* Patrick Dowd, Erol Gelenbe, 1995
The proceedings of the January 1995 workshop held to discuss modeling and analysis of computer and communication systems comprise papers in honor of Edward G Coffman Jr and regular papers submitted for consideration Sessions are

devoted to systems queueing systems networking distributed simulation multiprocessor architecture distributed systems modeling techniques parallel systems processor network and system simulation optimizing parallel programs systems modeling Petri nets neural networks and genetic algorithms and real time and a session in honor of E G Coffman Jr s 60th birthday No index Annotation copyright by Book News Inc Portland OR *Academic Computing* ,1989 **Index to Theses with Abstracts Accepted for Higher Degrees by the Universities of Great Britain and Ireland and the Council for National Academic Awards** ,2008 Theses on any subject submitted by the academic libraries in the UK and Ireland

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