

SystemC Kernel Extensions for Heterogeneous System Modeling

A Framework for Multi-NoC Modeling & Simulation

By
Hiren D. Patel and Sandeep K. Shukla

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SystemC Kernel Extensions for Heterogeneous System Modeling Hiren Patel,Sandeep Kumar Shukla,2006-01-16

SystemC Kernel Extensions for Heterogeneous System Modeling is a result of an almost two year endeavour on our part to understand how SystemC can be made useful for system level modeling at higher levels of abstraction Making it a truly heterogeneous modeling language and platform for hardware software co design as well as complex embedded hardware designs has been our focus in the work reported in this book SystemC Kernel Extensions for Heterogeneous System Modeling Hiren Patel,Sandeep Kumar Shukla,2008-11-01 SystemC Kernel Extensions for Heterogeneous System Modeling is a result of an almost two year endeavour on our part to understand how SystemC can be made useful for system level modeling at higher levels of abstraction Making it a truly heterogeneous modeling language and platform for hardware software co design as well as complex embedded hardware designs has been our focus in the work reported in this book

Synthesis of Embedded Software Sandeep Kumar Shukla,Jean-Pierre Talpin,2010-08-05 Embedded software is ubiquitous today There are millions of lines of embedded code in smart phones and even more in systems responsible for automotive control avionics control weapons control and space missions Some of these are safety critical systems whose correctness timely response and reliability are of paramount importance These requirement pose new challenges to system designers This necessitates that a proper design science based on constructive correctness be developed Correct by construction design and synthesis of embedded software is done in a way so that post development verification is minimized and correct operation of embedded systems is maximized This book presents the state of the art in the design of safety critical embedded software It introduced readers to three major approaches to specification driven embedded software synthesis construction synchronous programming based approaches models of computation based approaches and an approach based on concurrent programming with a co design focused language It is an invaluable reference for practitioners and researchers concerned with improving the product development life cycle Hardware/Software Co-design for Heterogeneous Multi-core Platforms

Koen Bertels,2012-02-02 HW SW Co Design for Heterogeneous Multi Core Platforms describes the results and outcome of the FP6 project which focuses on the development of an integrated tool chain targeting a heterogeneous multi core platform comprising of a general purpose processor ARM or powerPC a DSP the diopsis and an FPGA The tool chain takes existing source code and proposes transformations and mappings such that legacy code can easily be ported to a modern multi core platform Downloadable software will be provided for simulation purposes Embedded Systems Specification and Design Languages Eugenio Villar,2008-05-15 This book is the latest contribution to the Chip Design Languages series and it consists of selected papers presented at the Forum on Specifications and Design Languages FDL 07 in September 2007 The book represents the state of the art in research and practice and it identifies new research directions It highlights the role of specification and modelling languages and presents practical experiences with specification and modelling languages

Model Driven Engineering Languages and Systems Dorina C. Petriu, Nicolas Rouquette, Oystein Haugen, 2010-09-22 The MODELS series of conferences is the premier venue for the exchange of innovative technical ideas and experiences focusing on a very important new technical discipline model driven software and systems engineering. The expansion of this discipline is a direct consequence of the increasing significance and success of model based methods in practice. Numerous efforts resulted in the invention of concepts, languages and tools for the definition, analysis, transformation and verification of domain specific modeling languages and general purpose modeling language standards as well as their use for software and systems engineering. MODELS 2010, the 13th edition of the conference series, took place in Oslo, Norway, October 3-8, 2010, along with numerous satellite workshops, symposia and tutorials. The conference was fortunate to have three prominent keynote speakers: Ole Lehrmann Madsen (Aarhus University, Denmark), Edward A. Lee (UC Berkeley, USA) and Pamela Zave (AT&T Laboratories, USA). To provide a broader forum for reporting on scientific progress as well as on experience stemming from practical applications of model based methods, the 2010 conference accepted submissions in two distinct tracks: Foundations and Applications. The primary objective of the first track is to present new research results dedicated to advancing the state of the art of the discipline, whereas the second aims to provide a realistic and verifiable picture of the current state of the practice of model based engineering so that the broader community could be better informed of the capabilities and successes of this relatively young discipline. This volume contains the final version of the papers accepted for presentation at the conference from both tracks.

Embedded Systems Design Based on Formal Models of Computation Ivan Radojevic, Zoran Salcic, 2011-06-15. Models of Computation for Heterogeneous Embedded Systems presents a model of computation for heterogeneous embedded systems called DFCharts. It targets heterogeneous systems by combining finite state machines (FSMs) with synchronous dataflow graphs (SDFGs). FSMs are connected in the same way as in Argos, a Statecharts variant with purely synchronous semantics, using three operators: synchronous, parallel refinement and hiding. The fourth operator, called asynchronous parallel, is introduced in DFCharts to connect FSMs with SDFGs. In the formal semantics of DFCharts, the operation of an SDFG is represented as an FSM. Using this representation, SDFGs are merged with FSMs so that the behaviour of a complete DFCharts specification can be expressed as a single flat FSM. This allows system properties to be verified globally. The practical application of DFCharts has been demonstrated by linking it to widely used system level languages: Java, Esterel and SystemC.

Ingredients for Successful System Level Design Methodology Hiren D. Patel, Sandeep Kumar Shukla, 2008-06-06. ESL or Electronic System Level is a buzz word these days in the electronic design automation (EDA) industry in design houses and in the academia. Even though numerous trade magazine articles have been written, quite a few books have been published that have attempted to define ESL, it is still not clear what exactly it entails. However, what seems clear to everyone is that the Register Transfer Level (RTL) languages are not adequate any more to be the design entry point for today's and tomorrow's complex electronic system design. There are multiple reasons for such

thoughts First the continued progression of the miniaturization of the silicon technology has led to the ability of putting almost a billion transistors on a single chip Second applications are becoming more and more complex and integrated with communication control ubiquitous and pervasive computing and hence the need for ever faster ever more reliable and more robust electronic systems is putting designers towards a productivity demand that is not sustainable without a fundamental change in the design methodologies Also the hardware and software functionalities are getting interchangeable and ability to model and design both in the same manner is gaining importance Given this context we assume that any methodology that allows us to model an entire electronic system from a system perspective rather than just hardware with discrete event or cycle based semantics is an ESL methodology of some kind **The British National Bibliography** Arthur James Wells,2004

Proceedings ,2005 **CASES** ... ,2006 **American Book Publishing Record** ,2004 *SystemC Kernel Extensions for Heterogeneous System Modeling* Hiren Patel,Sandeep Kumar Shukla,2008-11-01 *SystemC Kernel Extensions for Heterogeneous System Modeling* is a result of an almost two year endeavour on our part to understand how SystemC can be made useful for system level modeling at higher levels of abstraction Making it a truly heterogeneous modeling language and platform for hardware software co design as well as complex embedded hardware designs has been our focus in the work reported in this book **The ... International Conference on Distributed Computing Systems** ,1988

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