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# Systems and Software Verification

Model-Checking Techniques and Tools



Springer

# Systems And Software Verification Modelchecking Techniques And Tools

**Nicolas Navet, Stephan Merz**

## Systems And Software Verification Modelchecking Techniques And Tools:

### **Systems and Software Verification** B. Berard,M. Bidoit,A. Finkel,F. Laroussinie,A. Petit,L. Petrucci,P.

Schnoebelen,2013-04-17 Model checking is a powerful approach for the formal verification of software When applicable it automatically provides complete proofs of correctness or explains via counter examples why a system is not correct This book provides a basic introduction to this new technique The first part describes in simple terms the theoretical basis of model checking transition systems as a formal model of systems temporal logic as a formal language for behavioral properties and model checking algorithms The second part explains how to write rich and structured temporal logic specifications in practice while the third part surveys some of the major model checkers available *Systems and Software Verification* B.

Berard,M. Bidoit,A. Finkel,F. Laroussinie,A. Petit,L. Petrucci,P. Schnoebelen,2014-03-12 Model checking is a powerful approach for the formal verification of software It automatically provides complete proofs of correctness or explains via counter examples why a system is not correct Here the author provides a well written and basic introduction to the new technique The first part describes in simple terms the theoretical basis of model checking transition systems as a formal model of systems temporal logic as a formal language for behavioral properties and model checking algorithms The second part explains how to write rich and structured temporal logic specifications in practice while the third part surveys some of the major model checkers available *Handbook of Model Checking* Edmund M. Clarke,Thomas A. Henzinger,Helmut Veith,Roderick Bloem,2018-05-18 Model checking is a computer assisted method for the analysis of dynamical systems that can be modeled by state transition systems Drawing from research traditions in mathematical logic programming languages hardware design and theoretical computer science model checking is now widely used for the verification of hardware and software in industry The editors and authors of this handbook are among the world's leading researchers in this domain and the 32 contributed chapters present a thorough view of the origin theory and application of model checking In particular the editors classify the advances in this domain and the chapters of the handbook in terms of two recurrent themes that have

driven much of the research agenda the algorithmic challenge that is designing model checking algorithms that scale to real life problems and the modeling challenge that is extending the formalism beyond Kripke structures and temporal logic The book will be valuable for researchers and graduate students engaged with the development of formal methods and verification tools **Modeling and Verification of Real-time Systems** Nicolas Navet,Stephan Merz,2013-03-07 This title is devoted to presenting some of the most important concepts and techniques for describing real time systems and analyzing their behavior in order to enable the designer to achieve guarantees of temporal correctness Topics addressed include mathematical models of real time systems and associated formal verification techniques such as model checking probabilistic modeling and verification programming and description languages and validation approaches based on testing With contributions from authors who are experts in their respective fields this will provide the reader with the state of the art in

formal verification of real time systems and an overview of available software tools      **Hardware and Software, Verification and Testing** Eyal Bin,Avi Ziv,Shmuel Ur,2007-02-09 This book constitutes the thoroughly refereed post proceedings of the Second International Haifa Verification Conference HVC 2006 held in Haifa Israel in October 2006 The 15 revised full papers presented together with 2 invited lectures are organized in three topical tracks on hardware verification technologies and methodologies software testing and tools for hardware verification and software testing      *Verified Software: Theories, Tools, Experiments* Bertrand Meyer,James Woodcock,2008-07-07 A Step Towards Verified Software Worries about the reliability of software are as old as software itself techniques for allaying these worries predate even James King s 1969 thesis on A program verifier What gives the whole topic a new urgency is the conjunction of three phenomena the blitz like spread of software rich systems to control ever more facets of our world and our lives our growing impatience with deficiencies and the development proceeding more slowly alas than the other two trends of techniques to ensure and verify software quality In 2002 Tony Hoare one of the most distinguished contributors to these advances over the past four decades came to the conclusion that piecemeal efforts are no longer sufficient and proposed a Grand Challenge intended to achieve over 15 years the production of a verifying compiler a tool that while processing programs would also guarantee their adherence to specified properties of correctness robustness safety security and other desirable properties As Hoare sees it this endeavor is not a mere research project as might normally be carried out by one team or a small consortium of teams but a momentous endeavor comparable in its scope to the successful mission to send a man to the moon or to the sequencing of the human genome      *Tools for Practical Software Verification* Bertrand Meyer,Martin Nordio,2012-12-15 The LASER school is intended for professionals from the industry engineers and managers as well as university researchers including PhD students Participants learn about the most important software technology advances from the pioneers in the field The school s focus is applied although theory is welcome to establish solid foundations The format of the school favors extensive interaction between participants and speakers LASER 2011 is devoted to software verification tools There have been great advances in the field of software verification in recent years Today verification tools are being increasingly used not only by researchers but by programming practitioners The summer school will focus on several of the most prominent and practical of such tools from different areas of software verification such as formal proofs testing and model checking During the school the participants will not only learn the principles behind the tools but also get hands on experience trying the tools on real programs      *PROCEEDINGS OF THE 20TH CONFERENCE ON FORMAL METHODS IN COMPUTER-AIDED DESIGN - FMCAD 2020* Alexander Ivrii,Ofer Strichman,2020-09-11 Formal Methods in Computer Aided Design FMCAD is a conference series on the theory and applications of formal methods in hardware and system verification FMCAD provides a leading forum to researchers in academia and industry for presenting and discussing ground breaking methods technologies theoretical results and tools for reasoning formally about computing systems FMCAD covers formal

aspects of computer aided system design including verification specification synthesis and testing      **Model Checking Software** Corina S Pasareanu,2009-06-26 This book constitutes the refereed proceedings of the 16th International SPIN workshop on Model Checking Software SPIN 2009 held in Grenoble France in June 2009 The 15 revised full papers presented together with 3 tool papers and 4 invited talks were carefully reviewed and selected from 41 submissions The papers cover theoretical and algorithmic foundations as well as tools for software model checking by addressing theoretical advances and empirical evaluations related to state space and path exploration techniques as implemented in software verification tools      *25 Years of Model Checking* Orna Grumberg,Helmut Veith,2008-07-03 Model checking technology is among the foremost applications of logic to computer science and computer engineering The model checking community has achieved many breakthroughs bridging the gap between theoretical computer science and hardware and software engineering and it is reaching out to new challenging areas such as system biology and hybrid systems Model checking is extensively used in the hardware industry and has also been applied to the verification of many types of software Model checking has been introduced into computer science and electrical engineering curricula at universities worldwide and has become a universal tool for the analysis of systems This Festschrift volume published in celebration of the 25th Anniversary of Model Checking includes a collection of 11 invited papers based on talks at the symposium 25 Years of Model Checking 25MC which was part of the 18th International Conference on Computer Aided Verification CAV 2006 which in turn was part of the Federated Logic Conference FLoC 2006 held in Seattle WA USA in August 2006 Model checking is currently attracting considerable attention beyond the core technical community and the ACM Turing Award 2007 was given in recognition of the paradigm shifting work on this topic initiated a quarter century ago Here we honor that achievement with the inclusion of facsimile reprints of the visionary papers on model checking by Edmund Clarke and Allen Emerson and by Jean Pierre Queille and Joseph Sifakis      **Journal of Research and Practice in Information Technology** ,2004      **Model Checking Software** ,2005      **Verification, Model Checking, and Abstract Interpretation** ,2004      **Techniques and Tools for Model-based Design and Analysis of Embedded Real-time Software** Zonghua Gu,2004      *Proceedings of the ... International Workshop on Formal Methods for Industrial Critical Systems* ,2005      **Algebraic Methodology and Software Technology** ,2004      **Automated Technology for Verification and Analysis** Sungdeok Cha,Jin-Young Choi,Moonzoo Kim,Mahesh Viswanathan,2008-10-06 gramatKoreaUniversityandtheDepartmentofComputerScienceatKAISTfor nancials support We sincerely hope that the readers find the proceedings of ATVA 2008 informative and rewarding      **Web Engineering** ,2005      **Workshop on Industrial-Strength Formal Specification Techniques** ,1995      **The 14th IEEE International Conference on Automated Software Engineering** IEEE Computer Society,1999 Twenty five papers presented at the October 1999 conference are grouped into sessions having the broad topics of software synthesis requirements elicitation reuse test synthesis analysis verification transformation architecture and automated testing Among the topics are data

mining library reuse patterns in user selected applications industrial applications of software synthesis via category theory automated translation of UML models of architectures for verification and simulation using SPIN verification of picture generated code evolving object oriented designs with refactorings automatically detecting mismatches during component based and model based development and an overview of Lutess a specification based tool for testing synchronous software There are also 25 short papers that represent novel work not yet fully mature No subject index Annotation copyrighted by Book News Inc Portland OR

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