

SYNTHESIS TECHNIQUES AND OPTIMIZATIONS FOR RECONFIGURABLE SYSTEMS

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Synthesis Techniques And Optimizations For Reconfigurable Systems

John Schewel



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Synthesis Techniques and Optimization for Reconfigurable Systems discusses methods used to model reconfigurable applications at the system level many of which could be incorporated directly into modern compilers The book also discusses a framework for reconfigurable system synthesis which bridges the gap between application level compiler analysis and high level device synthesis The development of this framework discussed in Chapter 5 and the creation of application analysis which further optimize its output discussed in Chapters 7 8 and 9 represent over four years of rigorous investigation within UCLA s Embedded and Reconfigurable Laboratory ERLab and UCSB s Extensible Programmable and Reconfigurable Embedded SystemS EXPRESS Group The research of these systems has not yet matured and we continually strive to develop data and methods which will extend the collective understanding of reconfigurable system synthesis IEEE Circuits & Devices ,2006 **Embedded Software and Systems** ,2004 **American Doctoral Dissertations** ,2002 Dissertation Abstracts International ,2008 **The Fourth International Conference/Exhibition on High-Performance Computing in the Asia-Pacific Region, Beijing, China, May 14-17, 2000** ,2000 Digest of Technical Papers ,2001

Proceedings of ASP-DAC/VLSI Design 2002 ,2002 Papers from a January 2002 conference are organized into four sessions each on low power design synthesis testing layout and interconnects and technology as well as two sessions each on embedded systems verification and VLSI architecture one session on analog design and one session on hot c

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Field-programmable Logic and Applications ,2002 *Proceedings of PART '98* Kenneth A. Hawick, Heath A. James, 1998-10 The proceedings of the 5th Australasian Conference on Parallel and Real Time Systems PART 98 collects some of the exciting and innovative approaches to the problems of high performance computing At a time when the field is going through a shake out of commercial solutions the research presented here gives an insight into new hardware software and algorithms for achieving high performance on parallel and reconfigurable computing systems PART 98 covers networking and processing hardware innovations network traffic simulation modeling and routing algorithms operating systems software and control methods and parallel algorithms and applications Special emphasis is on algorithms for managing data and memory and reconfigurable systems Articles describe activities at both overview level as well as highly technical mathematical analyses of some algorithmic approaches **Proceedings of the ASP-DAC ... Asia and South Pacific Design Automation Conference** ,2002 **Proceedings** ,2000 **Parallel and Distributed Processing** ,1998 **SME Technical Paper** Society of Manufacturing Engineers, 2003 **1994 IEEE International Symposium on Circuits and**

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