

THE SYNTHESIS APPROACH TO DIGITAL SYSTEM DESIGN

Petra Michel
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Springer

Synthesis Approach To Digital System Design

Philippe Coussy, Adam Morawiec



Synthesis Approach To Digital System Design:

The Synthesis Approach to Digital System Design Petra Michel, Ulrich Lauther, Peter Duzy, 2012-12-06 Over the past decade there has been a dramatic change in the role played by design automation for electronic systems Ten years ago integrated circuit IC designers were content to use the computer for circuit logic and limited amounts of high level simulation as well as for capturing the digitized mask layouts used for IC manufacture The tools were only aids to design the designer could always find a way to implement the chip or board manually if the tools failed or if they did not give acceptable results Today however design technology plays an indispensable role in the design of electronic systems and is critical to achieving time to market cost and performance targets In less than ten years designers have come to rely on automatic or semi automatic CAD systems for the physical design of complex ICs containing over a million transistors In the past three years practical logic synthesis systems that take into account both cost and performance have become a commercial reality and many designers have already relinquished control of the logic netlist level of design to automatic computer aids To date only in certain well defined areas especially digital signal processing and telecommunications have higher level design methods and tools found significant success However the forces of time to market and growing system complexity will demand the broad based adoption of high level automated methods and tools over the next few years

Field-Programmable Logic: Architectures, Synthesis and Applications Reiner W. Hartenstein, 1994-08-24 This volume contains the proceedings of the 4th International Workshop on Field Programmable Logic and Applications FPL 94 held in Prague Czech Republic in September 1994 The growing importance of field programmable devices is substantiated by the remarkably high number of 116 submissions for FPL 94 from them the revised versions of 40 full papers and 24 high quality poster presentations were accepted for inclusion in this volume Among the topics treated are testing layout synthesis tools compilation research and CAD trade offs and experience innovations and smart applications FPGA based computer architectures high level design prototyping and ASIC emulators commercial devices new tools CCMs and HW SW co design modelers educational experience and novel architectures *The Computer Engineering Handbook* Vojin G.

Oklobdzija, 2001-12-26 There is arguably no field in greater need of a comprehensive handbook than computer engineering The unparalleled rate of technological advancement the explosion of computer applications and the now in progress migration to a wireless world have made it difficult for engineers to keep up with all the developments in specialties outside their own

Digital Design and Fabrication Vojin G. Oklobdzija, 2017-12-19 In response to tremendous growth and new technologies in the semiconductor industry this volume is organized into five information rich sections Digital Design and Fabrication surveys the latest advances in computer architecture and design as well as the technologies used to manufacture and test them Featuring contributions from leading experts the book also includes a new section on memory and storage in addition to a new chapter on nonvolatile memory technologies Developing advanced concepts this sharply focused book

Describes new technologies that have become driving factors for the electronic industry Includes new information on semiconductor memory circuits whose development best illustrates the phenomenal progress encountered by the fabrication and technology sector Contains a section dedicated to issues related to system power consumption Describes reliability and testability of computer systems Pinpoints trends and state of the art advances in fabrication and CMOS technologies Describes performance evaluation measures which are the bottom line from the user s point of view Discusses design techniques used to create modern computer systems including high speed computer arithmetic and high frequency design timing and clocking and PLL and DLL design

Readings in Hardware/Software Co-Design Giovanni De Micheli,Rolf Ernst,Wayne Wolf,2002 This title serves as an introduction and reference for the field with the papers that have shaped the hardware software co design since its inception in the early 90s

Advances in Computers ,1993-09-14 Advances in Computers

Digital Systems Design with VHDL and Synthesis Kou-Chuan Chang,1999-05-11 A result of K C Chang s practical experience in both design and as an instructor this book presents an integrated approach to digital design principles processes and implementations to help the reader design much more complex systems within a shorter design cycle Many of the design techniques and considerations illustrated throughout the chapters are examples of viable designs

Digital System Design with VHDL Mark Zwoliński,2000 Electronic systems based on digital principles are becoming ubiquitous A good design approach to these systems is essential and a top down methodology is favoured Such an approach is vastly simplified by the use of computer modeling to describe the systems VHDL is a formal language which allows a designer to model the behaviours and structure of a digital circuit on a computer before implementation Digital System Design with VHDL is intended both for students on Digital Design courses and practitioners who would like to integrate digital design and VHDL synthesis in the workplace Its unique approach combines the principles of digital design with a guide to the use of VHDL Synthesis issues are discussed and practical guidelines are provided for improving simulation accuracy and performance Features a practical perspective is obtained by the inclusion of real life examples an emphasis on software engineering practices encourages clear coding and adequate documentation of the process demonstrates the effects of particular coding styles on synthesis and simulation efficiency covers the major VHDL standards includes an appendix with examples in Verilog

Languages, Design Methods, and Tools for Electronic System Design Tom J. Kazmierski,Sebastian Steinhorst,Daniel Große,2019-12-20 This book brings together a selection of the best papers from the twenty first edition of the Forum on specification and Design Languages Conference FDL which took place on September 10 12 2018 in Munich Germany FDL is a well established international forum devoted to dissemination of research results practical experiences and new ideas in the application of specification design and verification languages to the design modeling and verification of integrated circuits complex hardware software embedded systems and mixed technology systems Covers Assertion Based Design Verification Includes language based modeling and design techniques for embedded systems Covers design modeling

and verification of mixed physical domain and mixed signal systems that include significant analog parts in electrical and non electrical domains Includes formal and semi formal system level design methods for complex embedded systems based on the Unified Modelling Language UML and Model Driven Engineering MDE *High-Level Synthesis* Philippe Coussy,Adam Morawiec,2008-08-01 This book presents an excellent collection of contributions addressing different aspects of high level synthesis from both industry and academia It includes an overview of available EDA tool solutions and their applicability to design problems *1994 IEEE International Symposium on Circuits and Systems* ,1994 **System Architecture and Integration** Euromicro. Conference,1994 Euromicro 94 has the theme System Architecture and Integration The proceedings contain two keynote speeches The Design of Fault Tolerant Real Time Systems by H Kopetz and A Theory of Engineering Design by C A R Hoare and 87 technical papers in sessions including design and optimization database retrieval techniques mapping to parallel systems VLSI high level synthesis object oriented techniques VLSI testing and testability special architectures protocols tools for VLSI design specification and design dedicated devices expert and knowledge based systems parallel architectures application of mathematical models using distributed systems neural nets FSM synthesis and fault tolerance in parallel systems No index Annotation copyright by Book News Inc Portland OR **The Fifth International Workshop on Rapid System Prototyping** Nick Kanopoulos,1994 Proceedings of the June 1994 workshop presenting 25 papers on models for system simulation emulation in a hierarchical sense software to hardware mapping software prototyping and validation prototyping environments of hardware simulators and experiences from specific system prototyping projects Lacks an index Annotation copyright by Book News Inc Portland OR **Digital BiCMOS Integrated Circuit Design** Sherif H.K. Embabi,Abdellatif Bellaouar,Mohamed I. Elmasry,1993 Digital BiCMOS Integrated Circuit Design is the first book devoted entirely to the analysis and design of digital BiCMOS integrated circuits BiCMOS Integrated Circuit Design also reviews CMOS and CML integrated circuit design The application of BiCMOS in the design of digital subsystems e g adders multipliers RAMs and PLAs is addressed The book also introduces the reader to IC process technology CMOS bipolar and BiCMOS The modeling of both the bipolar and MOS devices are covered Many process device circuit design issues are discussed Digital BiCMOS Integrated Circuit Design can be used by engineers researchers graduate and senior undergraduate students working in the area of digital integrated circuits digital circuits and system design BiCMOS process and device modeling **Field-programmable Logic and Applications** ,1996 **Proceedings, 11th IEEE International Conference and Workshop on the Engineering of Computer-Based Systems** Vaclav Dvorak,Miroslav Sveda,2004

Computer Hardware Description Languages and Their Applications David Agnew,Luc J. M. Claesen,Raul Camposano,1993 Hardware description languages HDLs have established themselves as one of the principal means of designing electronic systems The interest in and usage of HDLs continues to spread rapidly driven by the increasing complexity of systems the growth of HDL driven synthesis the research on formal design methods and many other related

advances This research oriented publication aims to make a strong contribution to further developments in the field The following topics are explored in depth BDD based system design and analysis system level formal verification formal reasoning on hardware languages for protocol specification VHDL HDL based design methods high level synthesis and text graphical HDLs There are short papers covering advanced design capture and recent work in high level synthesis and formal verification In addition several invited presentations on key issues discuss and summarize recent advances in real time system design automatic verification of sequential circuits and languages for protocol specification Proceedings, ... International Symposium on VLSI Design ,1996 **The SECD Microprocessor** Brian T. Graham,1992-05-31 Very Good No Highlights or Markup all pages are intact Science Abstracts ,1993

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