

Solid-state Circuits 2004 International Conference

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Solid State Circuits 2004 International Conference

Takayuki Kawahara, Hiroyuki Mizuno



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CMOS Time-Mode Circuits and Systems Fei Yuan, 2018-09-03

Time mode circuits where information is represented by time difference between digital events offer a viable and technology friendly means to realize mixed mode circuits and systems in nanometer complementary metal oxide semiconductor CMOS technologies Various architectures of time based signal processing and design techniques of CMOS time mode circuits have emerged however an in depth examination of the principles of time based signal processing and design techniques of time mode circuits has not been available until now CMOS Time Mode Circuits and Systems Fundamentals and Applications is the first book to deliver a comprehensive treatment of CMOS time mode circuits and systems Featuring contributions from leading experts this authoritative text contains a rich collection of literature on time mode circuits and systems The book begins by presenting a critical comparison of voltage mode current mode and time mode signaling for mixed mode signal processing and then Covers the fundamentals of time mode signal processing such as voltage to time converters all digital phase locked loops and frequency synthesizers Investigates the performance characteristics architecture design techniques and implementation of time to digital converters Discusses time mode delta sigma based analog to digital converters placing a great emphasis on time mode quantizers Includes a detailed study of ultra low power integrated time mode temperature measurement systems CMOS Time Mode Circuits and Systems Fundamentals and Applications provides a valuable reference for circuit design engineers hardware system engineers graduate students and others seeking to master this fast evolving field

Advanced VLSI Design and Testability Issues Suman Lata Tripathi, Sobhit Saxena, Sushanta Kumar

Mohapatra, 2020-08-18 This book facilitates the VLSI interested individuals with not only in depth knowledge but also the broad aspects of it by explaining its applications in different fields including image processing and biomedical The deep understanding of basic concepts gives you the power to develop a new application aspect which is very well taken care of in this book by using simple language in explaining the concepts In the VLSI world the importance of hardware description languages cannot be ignored as the designing of such dense and complex circuits is not possible without them Both Verilog and VHDL languages are used here for designing The current needs of high performance integrated circuits ICs including low power devices and new emerging materials which can play a very important role in achieving new functionalities are the most interesting part of the book The testing of VLSI circuits becomes more crucial than the designing of the circuits in this nanometer technology era The role of fault simulation algorithms is very well explained and its implementation using Verilog is the key aspect of this book This book is well organized into 20 chapters Chapter 1 emphasizes on uses of FPGA on various image processing and biomedical applications Then the descriptions enlighten the basic understanding of digital design from the perspective of HDL in Chapters 2 5 The performance enhancement with alternate material or geometry for silicon based FET designs is focused in Chapters 6 and 7 Chapters 8 and 9 describe the study of bimolecular interactions with biosensing

FETs Chapters 10-13 deal with advanced FET structures available in various shapes materials such as nanowire HFET and their comparison in terms of device performance metrics calculation Chapters 14-18 describe different application specific VLSI design techniques and challenges for analog and digital circuit designs Chapter 19 explains the VLSI testability issues with the description of simulation and its categorization into logic and fault simulation for test pattern generation using Verilog HDL Chapter 20 deals with a secured VLSI design with hardware obfuscation by hiding the IC's structure and function which makes it much more difficult to reverse engineer

Green Computing with Emerging Memory Takayuki Kawahara, Hiroyuki Mizuno, 2012-09-26 This book describes computing innovation using non-volatile memory for a sustainable world It appeals to both computing engineers and device engineers by describing a new means of lower power computing innovation without sacrificing performance over conventional low voltage operation Readers will be introduced to methods of design and implementation for non-volatile memory which allow computing equipment to be turned off normally when not in use and to be turned on instantly to operate with full performance when needed

Optical Wireless Communications Roberto Ramirez-Iniguez, Sevia M. Idrus, Ziran Sun, 2008-04-03 Over the last three decades interest in Infrared IR technology as a medium to convey information has grown considerably This is reflected by the increasing number of devices such as laptops PDAs and mobile phones that incorporate optical wireless transceivers and also by the increasing number of optical wireless links available for indoor and

2004 7th International Conference on Solid-State and Integrated Circuits Technology, 2004-01-01

High-Performance D/A-Converters Martin Clara, 2012-12-22 This book deals with modeling and implementation of high performance current steering D/A converters for digital transceivers in nanometer CMOS technology In the first part the fundamental performance limitations of current steering DACs are discussed Based on simplified models closed form expressions for a number of basic non-ideal effects are derived and tested With the knowledge of basic performance limits the converter and system architecture can be optimized in an early design phase trading off circuit complexity silicon area and power dissipation for static and dynamic performance The second part describes four different current steering DAC designs in standard 130 nm CMOS The converters have a resolution in the range of 12-14 bits for an analog bandwidth between 2.2 MHz and 50 MHz and sampling rates from 100 MHz to 350 MHz Dynamic Element Matching DEM and advanced dynamic current calibration techniques are employed to minimize the required silicon area

Comprehensive Semiconductor Science and Technology, 2011-01-28 Semiconductors are at the heart of modern living Almost everything we do be it work travel communication or entertainment all depend on some feature of semiconductor technology Comprehensive Semiconductor Science and Technology Six Volume Set captures the breadth of this important field and presents it in a single source to the large audience who study make and exploit semiconductors Previous attempts at this achievement have been abbreviated and have omitted important topics Written and Edited by a truly international team of experts this work delivers an objective yet cohesive global review of the semiconductor world The

work is divided into three sections The first section is concerned with the fundamental physics of semiconductors showing how the electronic features and the lattice dynamics change drastically when systems vary from bulk to a low dimensional structure and further to a nanometer size Throughout this section there is an emphasis on the full understanding of the underlying physics The second section deals largely with the transformation of the conceptual framework of solid state physics into devices and systems which require the growth of extremely high purity nearly defect free bulk and epitaxial materials The last section is devoted to exploitation of the knowledge described in the previous sections to highlight the spectrum of devices we see all around us Provides a comprehensive global picture of the semiconductor world Each of the work s three sections presents a complete description of one aspect of the whole Written and Edited by a truly international team of experts SBCCI 2007 Antonio Petraglia, Association for Computing Machinery, 2007 **Tradeoffs and**

Optimization in Analog CMOS Design David Binkley, 2008-09-15 Analog CMOS integrated circuits are in widespread use for communications entertainment multimedia biomedical and many other applications that interface with the physical world Although analog CMOS design is greatly complicated by the design choices of drain current channel width and channel length present for every MOS device in a circuit these design choices afford significant opportunities for optimizing circuit performance This book addresses tradeoffs and optimization of device and circuit performance for selections of the drain current inversion coefficient and channel length where channel width is implicitly considered The inversion coefficient is used as a technology independent measure of MOS inversion that permits design freely in weak moderate and strong inversion This book details the significant performance tradeoffs available in analog CMOS design and guides the designer towards optimum design by describing An interpretation of MOS modeling for the analog designer motivated by the EKV MOS model using tabulated hand expressions and figures that give performance and tradeoffs for the design choices of drain current inversion coefficient and channel length performance includes effective gate source bias and drain source saturation voltages transconductance efficiency transconductance distortion normalized drain source conductance capacitances gain and bandwidth measures thermal and flicker noise mismatch and gate and drain leakage current Measured data that validates the inclusion of important small geometry effects like velocity saturation vertical field mobility reduction drain induced barrier lowering and inversion level increases in gate referred flicker noise voltage In depth treatment of moderate inversion which offers low bias compliance voltages high transconductance efficiency and good immunity to velocity saturation effects for circuits designed in modern low voltage processes Fabricated design examples that include operational transconductance amplifiers optimized for various tradeoffs in DC and AC performance and micropower low noise preamplifiers optimized for minimum thermal and flicker noise A design spreadsheet available at the book web site that facilitates rapid optimum design of MOS devices and circuits Tradeoffs and Optimization in Analog CMOS Design is the first book dedicated to this important topic It will help practicing analog circuit designers and advanced students of electrical

engineering build design intuition rapidly optimize circuit performance during initial design and minimize trial and error circuit simulations **Proceedings** ,2007 PLANS 2004 ,2004 **High Performance and Energy Efficient Multi-core Systems for DSP Applications** Zhiyi Yu,2007 **2004 7th International Conference on Solid-State and Integrated Circuits Technology** ,2004 *International Congress Calendar* ,2004 **A Multi-sensor Microsystem for Autonomous Data Gathering** David Frederick Lemmerhirt,2005 **Proceedings of the CANEUS2006 MNT for Aerospace Applications** Milind Pimprikar,Minoo Dastoor,2006 **EBlocks** Susan J. Lysecky,2006 **Proceedings** International Conference on Solid State and Integrated Circuits Technology,Ru Huang,Institute of Electrical and Electronics Engineers Beijing Section,Zhongguo-Dianzi-Xuehui,2004 *Frontiers in Electronics* H. Iwai,2006 Frontiers in Electronics reports on the most recent developments and future trends in the electronics and photonics industry The issues address CMOS SOI and wide band gap semiconductor technology terahertz technology and bioelectronics providing a unique interdisciplinary overview of the key emerging issues This volume accurately reflects the recent research and development trends from pure research to research and development and its contributors are leading experts in microelectronics nanoelectronics and nanophotonics from academia industry and government agencies

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