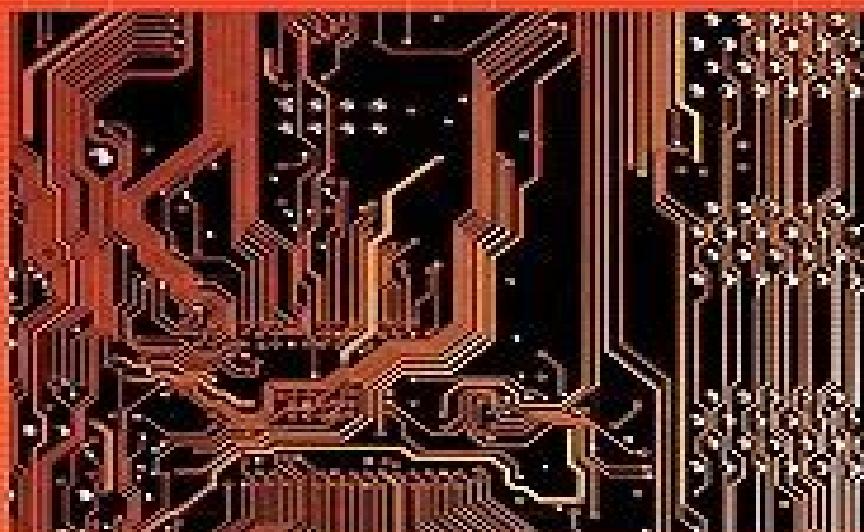


Symbolic Analysis and Reduction of VLSI Circuits

Chung-Kuan Cheng, Zhanhai Qin and Sheldon Tan



Symbolic Analysis And Reduction Of Vlsi Circuits

**Mourad Fakhfakh, Esteban Tlelo-
Cuautle, Francisco V. Fernández**

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Symbolic Analysis and Reduction of VLSI Circuits Zhanhai Qin, Chung-Kuan Cheng, 2009-03-13 Symbolic analysis is an intriguing topic in VLSI designs. The analysis methods are crucial for the applications to the parasitic reduction and analog circuit evaluation. However, analyzing circuits symbolically remains a challenging research issue. Therefore, in this book we survey the recent results as the progress of on-going works rather than as the solution of the field. For parasitic reduction we approximate a huge amount of electrical parameters into a simplified RLC network. This reduction allows us to handle very large integrated circuits with given memory capacity and CPU time. A symbolic analysis approach reduces the circuit according to the network topology. Thus the designer can maintain the meaning of the original network and perform the analysis hierarchically. For analog circuit designs, symbolic analysis provides the relation between the tunable parameters and the characteristics of the circuit. The analysis allows us to optimize the circuit behavior. The book is divided into three parts. Part I touches on the basics of circuit analysis in time domain and in s domain. For an s domain expression, the Taylor s expansion with s approaching infinity is equivalent to the time domain solution after the inverse Laplace transform. On the other hand, the Taylor s expansion when s approaches zero derives the moments of the output responses in time domain. Part II focuses on the techniques for parasitic reduction. In Chapter 2 we present the approximation methods to match the first few moments with reduced circuit orders. In Chapter 3 we apply the Y Delta transformation to reduce the dynamic linear network. The method finds the exact values of the low order coefficients of the numerator and denominator of the transfer function and thus matches part of the moments. In Chapter 4 we handle two major issues of the Y Delta transformation: common factors in fractional expressions and round off errors. Chapter 5 explains the stability of the reduced expression in particular the Ruth Hurwitz Criterion. We make an effort to describe the proof of the Criterion because the details are omitted in most of the contemporary textbooks. In Chapter 6 we present techniques to synthesize circuits to approximate the reduced expressions after the transformation. In Part III we discuss symbolic generation of the determinants and cofactors for the application to analog designs. In Chapter 7 we depict the classical topological analysis approach. In Chapter 8 we describe a determinant decision diagram approach that exploits the sparsity of the matrix to accelerate the computation. In Chapter 9 we take only significant terms when we search through determinant decision diagram to approximate the solution. In Chapter 10 we extend the determinant decision diagram to a hierarchical model. The construction of the modules through the hierarchy is similar to the Y Delta transformation in the sense that a byproduct of common factors appears in the numerator and denominator. Therefore we describe the method to prune the common factors.

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Martin,2018-10-03 Presenting a comprehensive overview of the design automation algorithms tools and methodologies used to design integrated circuits the Electronic Design Automation for Integrated Circuits Handbook is available in two volumes The second volume EDA for IC Implementation Circuit Design and Process Technology thoroughly examines real time logic to GDSII a file format used to transfer data of semiconductor physical layout analog mixed signal design physical verification and technology CAD TCAD Chapters contributed by leading experts authoritatively discuss design for manufacturability at the nanoscale power supply network design and analysis design modeling and much more Save on the complete set **1992**

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Circuits & Devices ,2003 VLSI: Systems on a Chip Luis Miguel Silveira,Srinivas Devadas,Ricardo A. Reis,1999-12-31

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Abstracts ,1997 **Behavioral Modeling and Simulation of Analog Circuits** Junjie Yang,2005 **Eleventh**

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The Circuits and Filters Handbook Wai-Kai Chen,1995-06-29 This invaluable reference book features the

most comprehensive coverage ever of circuits and filters from classical to state of the art designs It begins with a discussion of basic mathematics for signal processing and circuit and filter design then goes on to investigate the underlying theory and applications including a thorough analysis of both analog and digital circuits and filters

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Table of Contents Symbolic Analysis And Reduction Of Vlsi Circuits

1. Understanding the eBook Symbolic Analysis And Reduction Of Vlsi Circuits
 - The Rise of Digital Reading Symbolic Analysis And Reduction Of Vlsi Circuits
 - Advantages of eBooks Over Traditional Books
2. Identifying Symbolic Analysis And Reduction Of Vlsi Circuits
 - Exploring Different Genres
 - Considering Fiction vs. Non-Fiction
 - Determining Your Reading Goals
3. Choosing the Right eBook Platform
 - Popular eBook Platforms
 - Features to Look for in an Symbolic Analysis And Reduction Of Vlsi Circuits
 - User-Friendly Interface

4. Exploring eBook Recommendations from Symbolic Analysis And Reduction Of Vlsi Circuits
 - Personalized Recommendations
 - Symbolic Analysis And Reduction Of Vlsi Circuits User Reviews and Ratings
 - Symbolic Analysis And Reduction Of Vlsi Circuits and Bestseller Lists
5. Accessing Symbolic Analysis And Reduction Of Vlsi Circuits Free and Paid eBooks
 - Symbolic Analysis And Reduction Of Vlsi Circuits Public Domain eBooks
 - Symbolic Analysis And Reduction Of Vlsi Circuits eBook Subscription Services
 - Symbolic Analysis And Reduction Of Vlsi Circuits Budget-Friendly Options
6. Navigating Symbolic Analysis And Reduction Of Vlsi Circuits eBook Formats
 - ePUB, PDF, MOBI, and More
 - Symbolic Analysis And Reduction Of Vlsi Circuits Compatibility with Devices
 - Symbolic Analysis And Reduction Of Vlsi Circuits Enhanced eBook Features
7. Enhancing Your Reading Experience
 - Adjustable Fonts and Text Sizes of Symbolic Analysis And Reduction Of Vlsi Circuits
 - Highlighting and Note-Taking Symbolic Analysis And Reduction Of Vlsi Circuits
 - Interactive Elements Symbolic Analysis And Reduction Of Vlsi Circuits
8. Staying Engaged with Symbolic Analysis And Reduction Of Vlsi Circuits
 - Joining Online Reading Communities
 - Participating in Virtual Book Clubs
 - Following Authors and Publishers Symbolic Analysis And Reduction Of Vlsi Circuits
9. Balancing eBooks and Physical Books Symbolic Analysis And Reduction Of Vlsi Circuits
 - Benefits of a Digital Library
 - Creating a Diverse Reading Collection Symbolic Analysis And Reduction Of Vlsi Circuits
10. Overcoming Reading Challenges
 - Dealing with Digital Eye Strain
 - Minimizing Distractions
 - Managing Screen Time
11. Cultivating a Reading Routine Symbolic Analysis And Reduction Of Vlsi Circuits
 - Setting Reading Goals Symbolic Analysis And Reduction Of Vlsi Circuits
 - Carving Out Dedicated Reading Time

12. Sourcing Reliable Information of Symbolic Analysis And Reduction Of Vlsi Circuits
 - Fact-Checking eBook Content of Symbolic Analysis And Reduction Of Vlsi Circuits
 - Distinguishing Credible Sources
13. Promoting Lifelong Learning
 - Utilizing eBooks for Skill Development
 - Exploring Educational eBooks
14. Embracing eBook Trends
 - Integration of Multimedia Elements
 - Interactive and Gamified eBooks

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