

RCA

**Designer's
Handbook**

Solid-State Power Circuits

A comprehensive text on basic theory, technology, and circuit applications of power transistors, thyristors, silicon rectifiers, and power hybrid circuits.

Solid State Power Circuits Designer Handbook

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Solid-state Power Circuits RCA Corporation. Solid State Division, 1971 **Designer's Handbook** RCA, 1971 **RCA Solid-state Power Circuits Designer's Handbook** RCA Corporation. Solid State Division, 1971 Guide to the design of solid state power circuits for both linear and switching service **RCA Designer's Handbook**, 2004 **Semiconductor Power Electronics** Richard G. Hoft, 2012-12-06 Semiconductors have been used widely in signal level or brain applications Since their invention in 1948 transistors have revolutionized the electronics industry in computers information processing and communications Now however semiconductors are being used more and more where considerable power is required Devices such as high power bipolar junction transistors and power field effect transistors as well as SCRs TRIACs GTOs and other semiconductor switching devices that use a p n p n regenerative effect to achieve bistable action are expanding the power handling horizons of semiconductors and finding increasing application in a wide range of products including regulated power supplies lamp dimmers motor drives pulse modulators and heat controls HVDC and electric vehicle propulsion are two additional areas of application which may have a very significant long range impact on the technology The impact of solid state devices capable of handling appreciable power levels has yet to be fully realized Since it first became available in late 1957 the SCR or silicon controlled rectifier also called the reverse blocking triode thyristor has become the most popular member of the thyristor family At present SCRs are available from a large number of manufacturers in this country and abroad SCR ratings range from less than one ampere to over three thousand amperes with voltage ratings in excess of three thousand volts **Introduction to Solid State Power Electronics** William E. Newell, John William Motto, 1977

Handbook of Simplified Solid State Circuit Design John D. Lenk, 1978 Explains the rules involved in selecting components for specific transistor circuits **5G and E-Band Communication Circuits in Deep-Scaled CMOS** Marco Vigilante, Patrick Reynaert, 2018-02-07 This book discusses design techniques layout details and measurements of several key analog building blocks that currently limit the performance of 5G and E Band transceivers implemented in deep scaled CMOS The authors present recent developments in low noise quadrature VCOs and tunable inductor less frequency dividers Moreover the design of low loss broadband transformer based filters that realize inter stage matching power division combining and impedance transformation is discussed in great detail The design and measurements of a low noise amplifier a downconverter and a highly linear power amplifier that leverage the proposed techniques are shown All the prototypes were realized in advanced nanometer scaled CMOS technologies without RF thick to metal option **Modeling in Analog**

Design Jean-Michel Bergé, Oz Levia, Jacques Rouillard, 2012-12-06 Modeling in Analog Design highlights some of the most pressing issues in the use of modeling techniques for design of analogue circuits Using models for circuit design gives designers the power to express directly the behaviour of parts of a circuit in addition to using other pre defined components There are numerous advantages to this new category of analog behavioral language In the short term by favouring the top

down design and raising the level of description abstraction this approach provides greater freedom of implementation and a higher degree of technology independence In the longer term analog synthesis and formal optimisation are targeted Modeling in Analog Design introduces the reader to two main language standards VHDL A and MHDL It goes on to provide in depth examples of the use of these languages to model analog devices The final part is devoted to the very important topic of modeling the thermal and electrothermal aspects of devices This book is essential reading for analog designers using behavioral languages and analog CAD tool development environments who have to provide the tools used by the designers

Electronic Design's Gold Book ,1983 *Library of Congress Catalogs* Library of Congress,1976 **Linear Integrated Circuits and MOS Devices: Application notes** RCA Corporation. Solid State Division,1974 **SPD-100 Databook: Semiconductor devices** RCA Corporation,RCA Corporation. Semiconductor & Materials Ddivision,1966 **Catalog of Copyright Entries. Third Series** Library of Congress. Copyright Office,1974 *Signal Integrity Effects in Custom IC and ASIC Designs* Raminderpal Singh,2001-12-12 offers a tutorial guide to IC designers who want to move to the next level of chip design by unlocking the secrets of signal integrity Jake Buurma Senior Vice President Worldwide Research Development Cadence Design Systems Inc Covers signal integrity effects in high performance Radio Frequency RF IC Brings together research papers from the past few years that address the broad range of issues faced by IC designers and CAD managers now and in the future A Wiley IEEE Press publication **Library of Congress Catalog** Library of Congress,1970 A cumulative list of works represented by Library of Congress printed cards **Linear Integrated Circuits and MOS Devices** RCA Corporation. Solid State Division,1973 **Electronic Design** ,1981 **High Frequency Circuit Design** James K. Hardy,1979 **Subject Catalog** Library of Congress,1970

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